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October 29, 2007

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450


Re: **CERTIFICATE OF CORRECTION**
U. S. Letters Patent No. 7,251,811
Issued: July 31, 2007
For: **CONTROLLING COMPATIBILITY LEVELS OF
BINARY TRANSLATIONS BETWEEN
INSTRUCTION SET ARCHITECTURES**
Inventor: Rosner, et al.
Our File No. 42390.P12485

Dear Sir:

Enclosed is the Certificate of Correction for the above-referenced patent.
This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C.
Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP



Edwin H. Taylor
Reg. No. 25,129

EHT/sn
Enclosures

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,251,811
DATED : July 31, 2007
INVENTOR(S) : Rosner et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 13, at line 34, delete "mare" and insert --more--.

In column 13, at line 41, delete the second occurrence of "system".

MAILING ADDRESS OF SENDER
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PATENT NO. **7,251,811**

Certificate of Correction (PTO Form 1050)-Amended